Electronic Version
Stylesheet Version v1.1.1

Claims

[c1] A multistage amplifier, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage; and
a power source line connected to said plurality of amplifiers;
wherein said power source line has a first power source line connected to at least the initial-stage amplifier from among said plurality of amplifiers, and a second power source line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier.

[c2] The multistage amplifier according to claim 1, wherein a bypass condenser is arranged between said second power source line and a ground line.

[c3] A multistage amplifier, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage; and
a ground line connected to said plurality of amplifiers;

wherein said ground line has a first ground line connected to at least the initial-stage amplifier from among said plurality of amplifiers, and a second ground line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier.

[c4] A multistage amplifier, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage; and
a ground line connected to said plurality of amplifiers;
wherein substrates held by said plurality of amplifiers are each connected to said ground line.

[c5] A multistage amplifier, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a power source line connected to said plurality of amplifiers; and
a ground line connected to said plurality of amplifiers;
wherein said power source line has a first power source line connected to at least the initial-stage amplifier from among said plurality of amplifiers, and a second power source line commonly connected to the remaining am-

plifiers except for at least said initial-stage amplifier.

[c6] The multistage amplifier according to claim 5, wherein said ground line has a first ground line connected to at least said initial-stage amplifier, and a second ground line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier.

[c7] The multistage amplifier according to claim 5, wherein substrates held by said plurality of amplifiers are each connected to said ground line.

[c8] A multistage amplifier, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage; and
a plurality of power source lines that are individually connected to said plurality of amplifiers.

[c9] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a power source line connected to said plurality of amplifiers; and

a power source pad connected to said power source line; wherein said power source line has a first power source line connected between at least the initial-stage amplifier from among said plurality of amplifiers and said power source pad, and a second power source line commonly connected between the remaining amplifiers except for at least said initial-stage amplifier and said power source pad.

[c10] The integrated circuit according to claim 9, wherein a bypass condenser is arranged between said second power source line and a ground line.

[c11] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a ground line connected to said plurality of amplifiers; and
a ground pad connected to said ground line;
wherein said ground line has a first ground line connected between at least the initial-stage amplifier from among said plurality of amplifiers and said ground pad, and a second ground line commonly connected between the remaining amplifiers except for at least said initial-stage amplifier and said ground pad.

[c12] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a ground line connected to said plurality of amplifiers; and
a ground pad connected to said ground line;
wherein substrates held by said plurality of amplifiers are each connected to said ground line.

[c13] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a power source line connected to said plurality of amplifiers;
a ground line connected to said plurality of amplifiers;
a power source pad connected to said power source line; and
a ground pad connected to said ground line;
wherein said power source line has a first power source line connected between at least the initial-stage amplifier from among said plurality of amplifiers and said power source pad, and a second power source line com-

monly connected between the remaining amplifiers except for at least said initial-stage amplifier and said power source pad.

[c14] The integrated circuit according to claim 13, wherein said ground line has a first ground line connected between at least said initial-stage amplifier and said ground pad, and a second ground line commonly connected between the remaining amplifiers except for at least said initial-stage amplifier and said ground pad.

[c15] The integrated circuit according to claim 13, wherein substrates held by said plurality of amplifiers are each connected to said ground line.

[c16] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;
a plurality of power source lines individually connected to said plurality of amplifiers; and
a power source pad commonly connected to said plurality of power source lines.

[c17] An integrated circuit, comprising:
a plurality of amplifiers that are constructed in a cascade

connection manner so as to amplify an input signal from a previous stage and output the amplified signal to a following stage;

a first power source line connected to at least the initial-stage amplifier from among said plurality of amplifiers;

a second power source line commonly connected to the remaining amplifiers except for at least said initial-stage amplifier; and

a power source pad commonly connected to said first and second power source lines.

[c18] An integrated circuit, comprising:

a plurality of processing circuits that are constructed in a cascade connection manner so as to process an input signal from a previous stage and output the processed signal to a following stage;

a first power source line connected to at least the initial-stage processing circuit from among said plurality of processing circuits;

a second source line commonly connected to the remaining processing circuits except for at least said initial-stage processing circuit; and

a power source pad commonly connected to said first and second power source lines.